

This is the block diagram for the sync circuitry.

FIG. 1

Detector Connecting Diagram

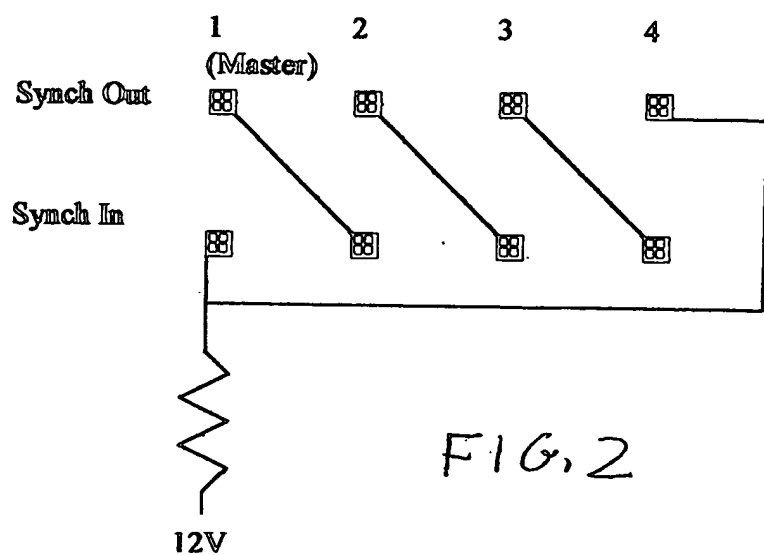


FIG. 2

Series Synch Operation Diagram

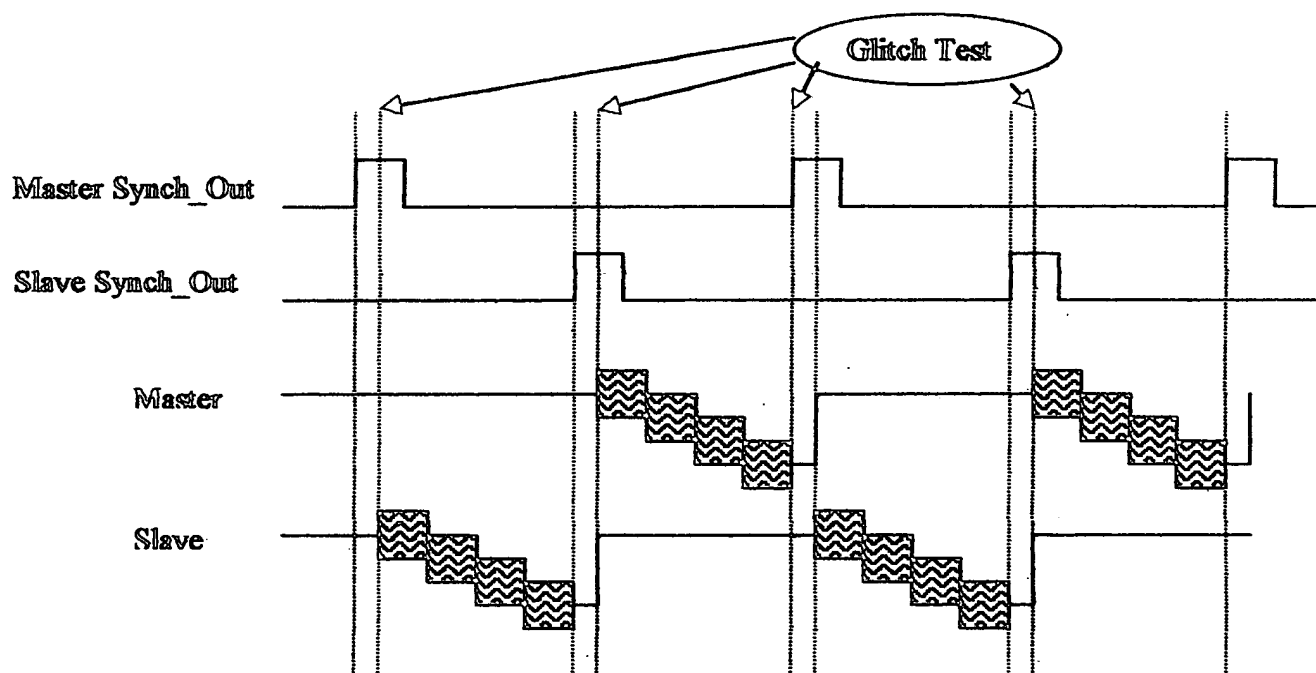
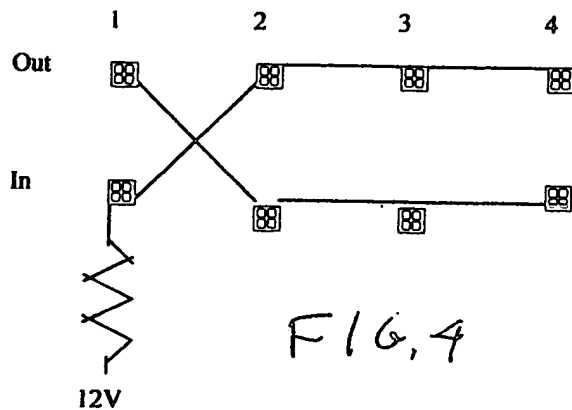


FIG. 3

# Detector Connecting Diagram



## Parallel Synch Operation Diagram

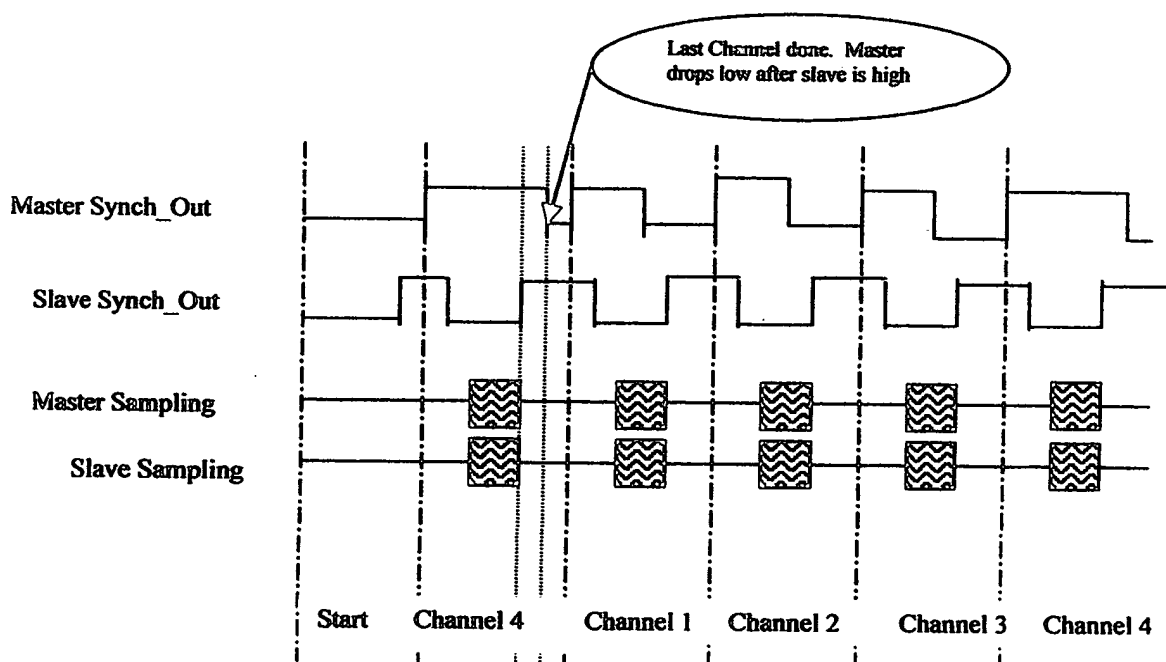


FIG. 5

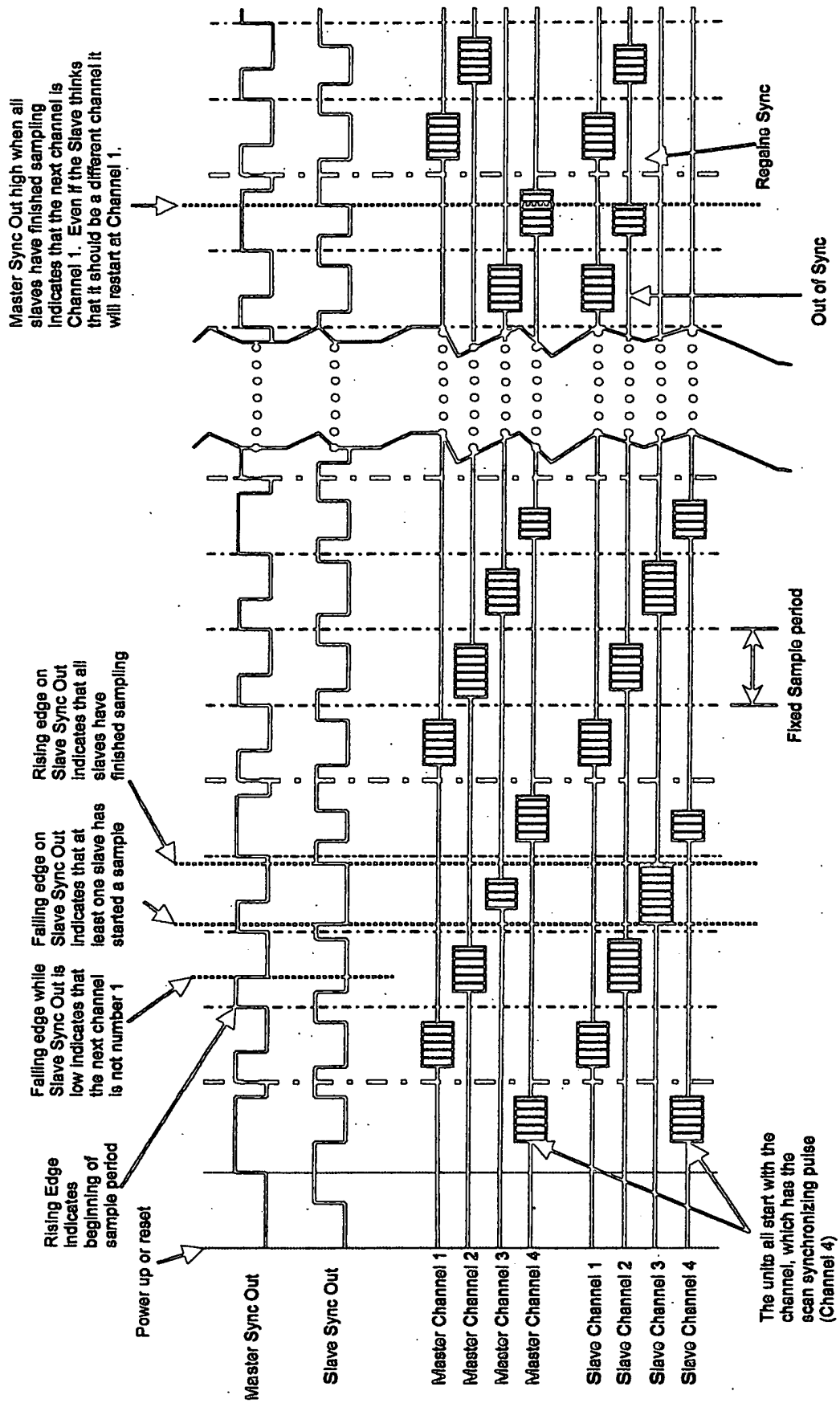


FIG. 6